

3C987 U.S. PTO  
10/005024  
11/04/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10005024	12/04/2001	439 36	803	2827	DINH, T

**\*\*APPLICANTS:** Hartke David; DiBene Joseph;

**\*\*CONTINUING DATA VERIFIED:**

This appln claims benefit of 60/251,222 12/04/2000  
and claims benefit of 60/251,223 12/04/2000  
and claims benefit of 60/251,184 12/04/2000  
and claims benefit of 60/266,941 02/06/2001  
and claims benefit of 60/277,369 03/19/2001  
and claims benefit of 60/287,860 05/01/2001  
and claims benefit of 60/291,749 05/16/2001  
and claims benefit of 60/291,772 05/16/2001  
and claims benefit of 60/292,125 05/18/2001  
and claims benefit of 60/299,573 06/19/2001  
and claims benefit of 60/301,753 06/27/2001  
and claims benefit of 60/304,929 07/11/2001 *to yes*  
and claims benefit of 60/304,930 07/11/2001  
and claims benefit of 60/310,038 08/03/2001  
and claims benefit of 60/313,338 08/17/2001  
and claims benefit of 60/338,004 11/08/2001  
and is a CIP of 09/885,780 06/19/2001 ABN  
which is a CIP of 09/353,428 07/15/1999 PAT 6,304,450  
This application 10/005,024

**\*\* FOREIGN APPLICATIONS VERIFIED:**

*to None*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		115.19USU1
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		
Verified and Acknowledged Examiners's initials <i>TD</i>			
<b>TITLE :</b> Integrated power delivery with flex circuit interconnection for high density high power circuits for integrated circuits and systems			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
Primary Examiner		Sheets Drwg.	Figs.Drwg.
PREPARED FOR ISSUE		Print Fig.	
Application Examiner			
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